

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,187,038 B2
APPLICATION NO. : 10/701165
DATED : March 6, 2007
INVENTOR(S) : Morin et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6, Line 20 Delete: "NMOS transistor and the at least one transistor;"
 Insert: --PMOS transistor and the at least one NMOS transistor;--

This certificate supersedes certificate of correction
issued August 7, 2007.

~~Signed and Sealed this~~

~~Sixth day of August, 2007~~



~~Director of the United States Patent and Trademark Office~~